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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Thomas, *et al.* Docket No.: 2003 P 54322 US
Serial No.: 10/790,907 Art Unit: 2822
Filed: March 2, 2004 Examiner: Bac H. Au
For: Integrated Circuit with Re-Route Layer and Stacked Die Assembly

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

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Respectfully submitted,

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